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The 8051 Microcontroller is one of the basic type of microcontroller, designed by Intel in 1980's. This microcontroller was based on Harvard Architecture and developed primarily for use in embedded systems technology. Normally, this microcontroller was developed using NMOS technology, which requires more power to operate.

[Microcontrollers - 8051 Architecture - Tutorials Point](#) 8051 microcontroller is designed by Intel in 1981. It is an 8-bit microcontroller. It is built with 40 pins DIP (dual inline package), 4kb of ROM storage and 128 bytes of RAM storage, 2 16-bit timers.

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[The 8051 Microcontroller Architecture. A microcontroller \(C\) is a single integrated circuit that executes a user program for controlling some device. A microcontroller has a CPU in addition to a fixed amount of RAM, ROM, I/O ports, serial port, and a timer all on a single chip.](#)

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8051 microcontroller architecture In this section we will discuss its internal architecture and main registers. Registers are mainly used while programming microcontrollers in assembly language.

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The 8051 Microcontroller is a 40-Pin integrated chip that comes with CPU and other peripherals like I/O, timers, counters, RAM and ROM. It is widely used in an

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embedded system, consumer electronics, automotive systems, robotics and security cameras.

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8051 Microcontroller Architecture Its possible to explain microcontroller architecture to a great detail, but we are limiting scope of this article to internal architecture, pin configuration, program memory and data memory organization.

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introduction block diagram and pin description of the 8051 registers some simple instructions structure of assembly language and running an 8051 program memory mapping in 8051

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8051 (microcontroller)class1 1. 8051

(MICROCONTROLLER) Nitin Ahire XIE, Mahim Prof. Nitin Ahire 1. 2. MICROCONTROLLER What is a microcontroller ? Basically a device which integrates a number of components of a microprocessor system on to a single chip, only need to supplied power and clocking. Microcontroller combines on the same chip 1) The CPU core 2) I/O Ports 3) Memory 4) Timer Prof. Nitin