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Discusses time-interleaved ADC theory in detail Presents and analyzes a full solution for timing-skew in time-interleaved ADCs Demonstrates how to optimize the design of a time-interleaved ADC for extremely low-power and very high sample rate

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This book describes techniques for time-interleaving a number of analog-to-digital data converters to achieve demanding bandwidth requirements. Readers will benefit from the presentation of a low-power solution that can be used in actual products, while alleviating the time-varying signal artifacts.

Background Calibration of Time-Interleaved Data Converters ...

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Who am I? Received B.E. from A.U.B. in 2004 Received Ph.D. from Stanford University in 2010 I Thesis: 'Background calibration of timing-skew in time-interleaved

